

Drawings

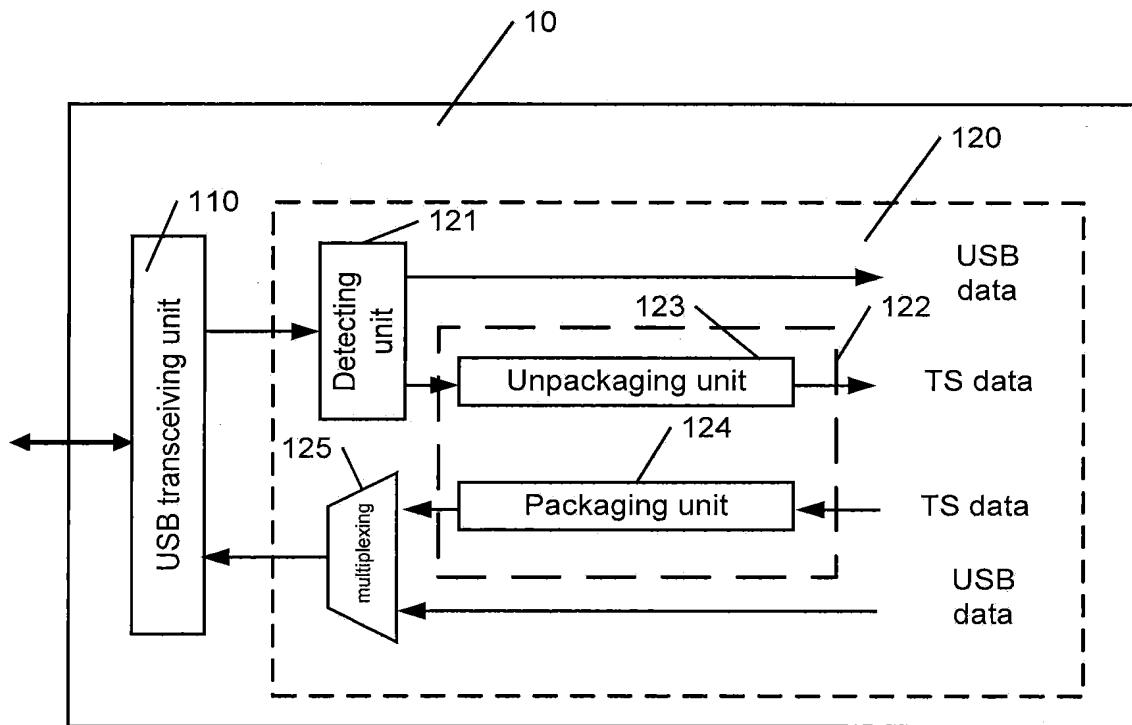


Fig.1

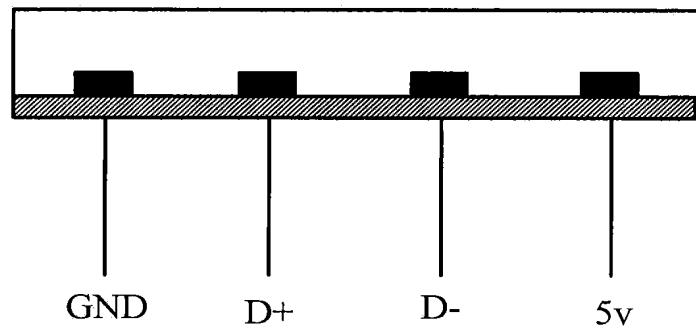


Fig.2

Field	Sync	PID	Data field	CRC
Bits	32	8	Nx188x8	16

Fig.3

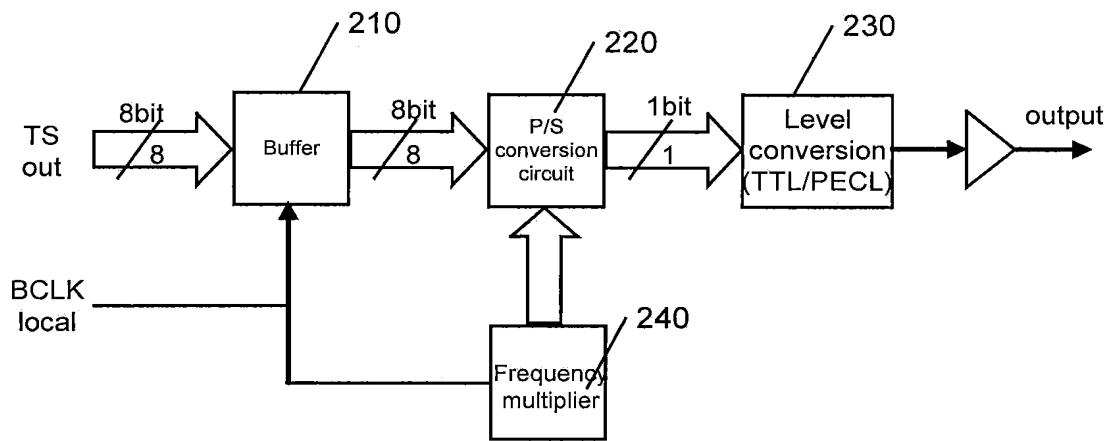


Fig.4

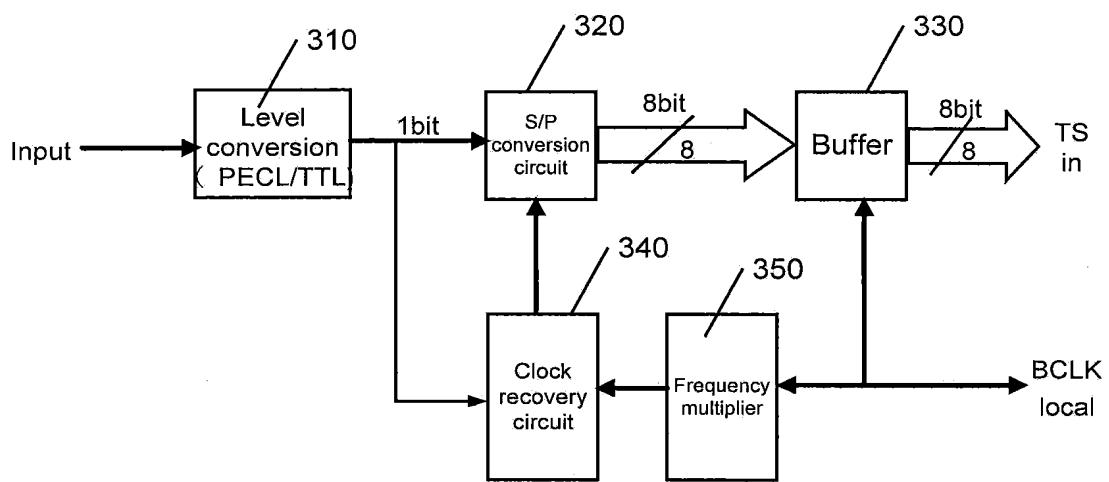


Fig.5

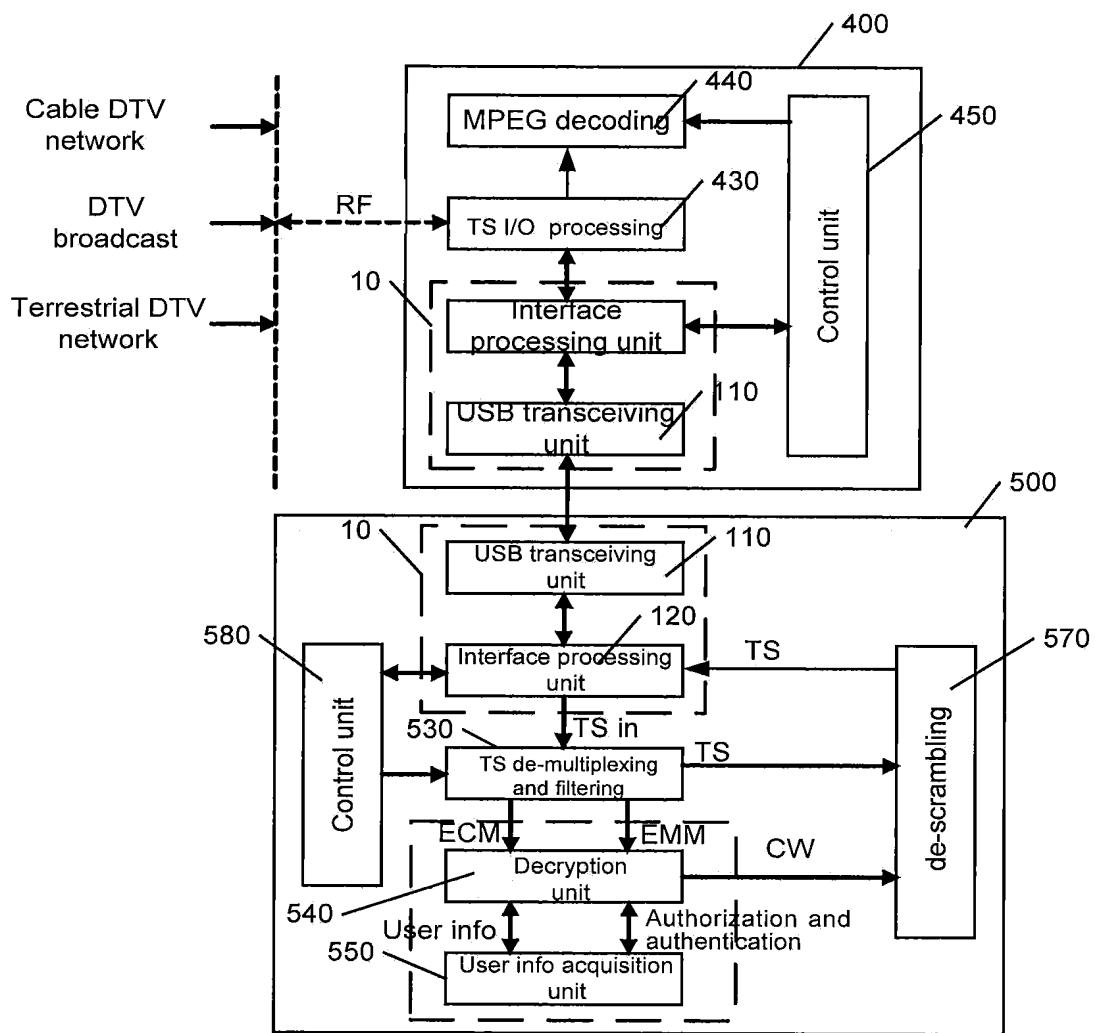


Fig.6

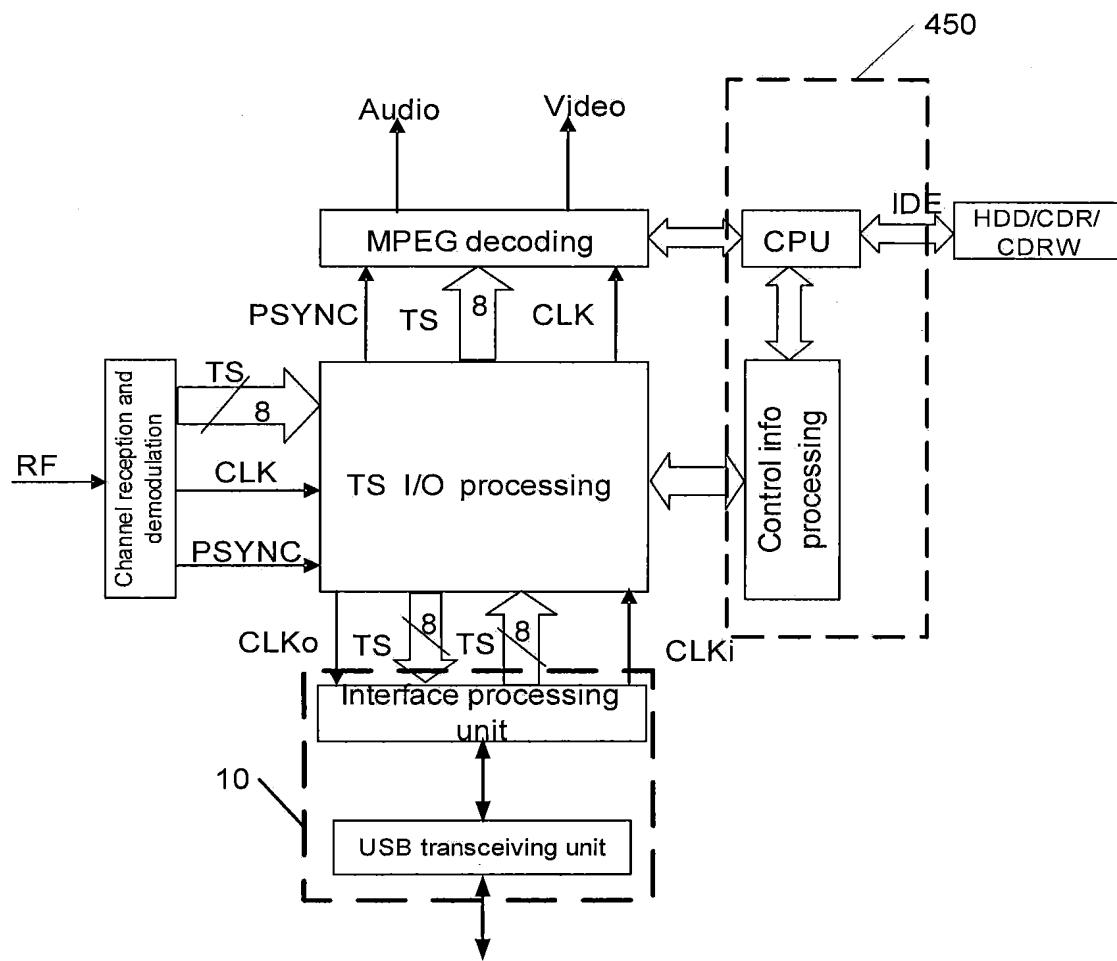


Fig.7

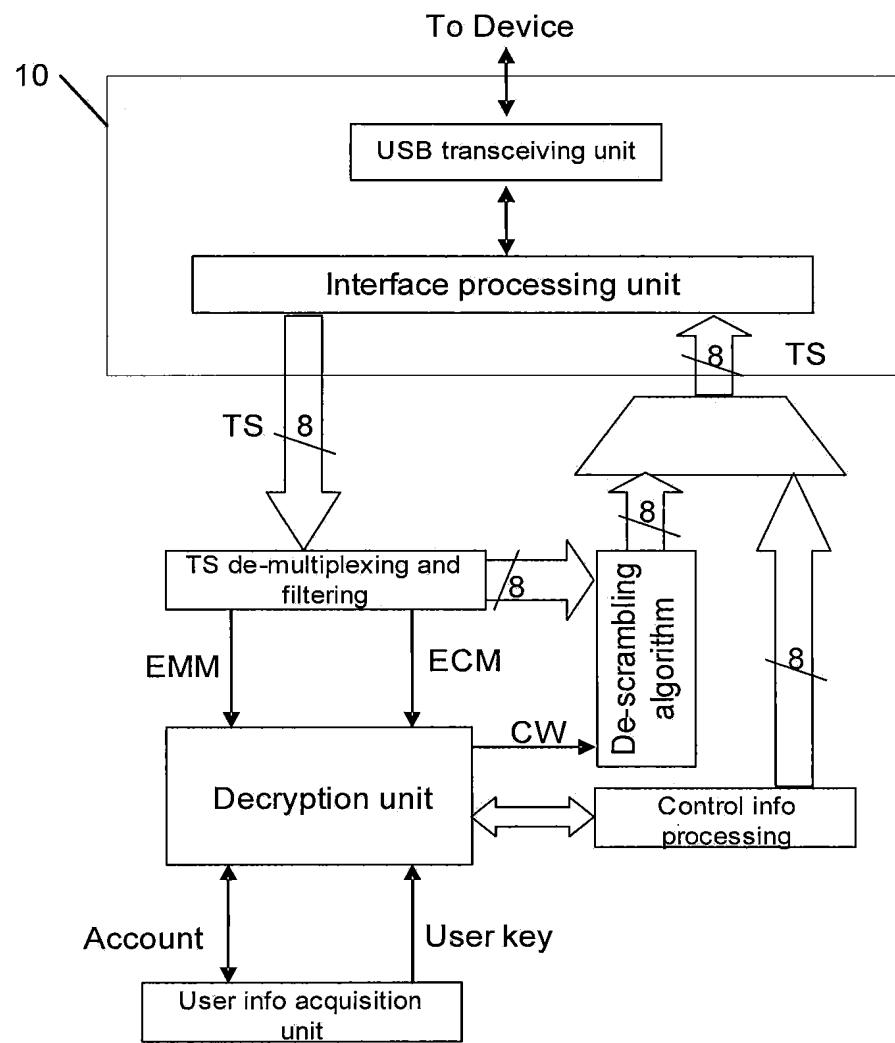


Fig.8

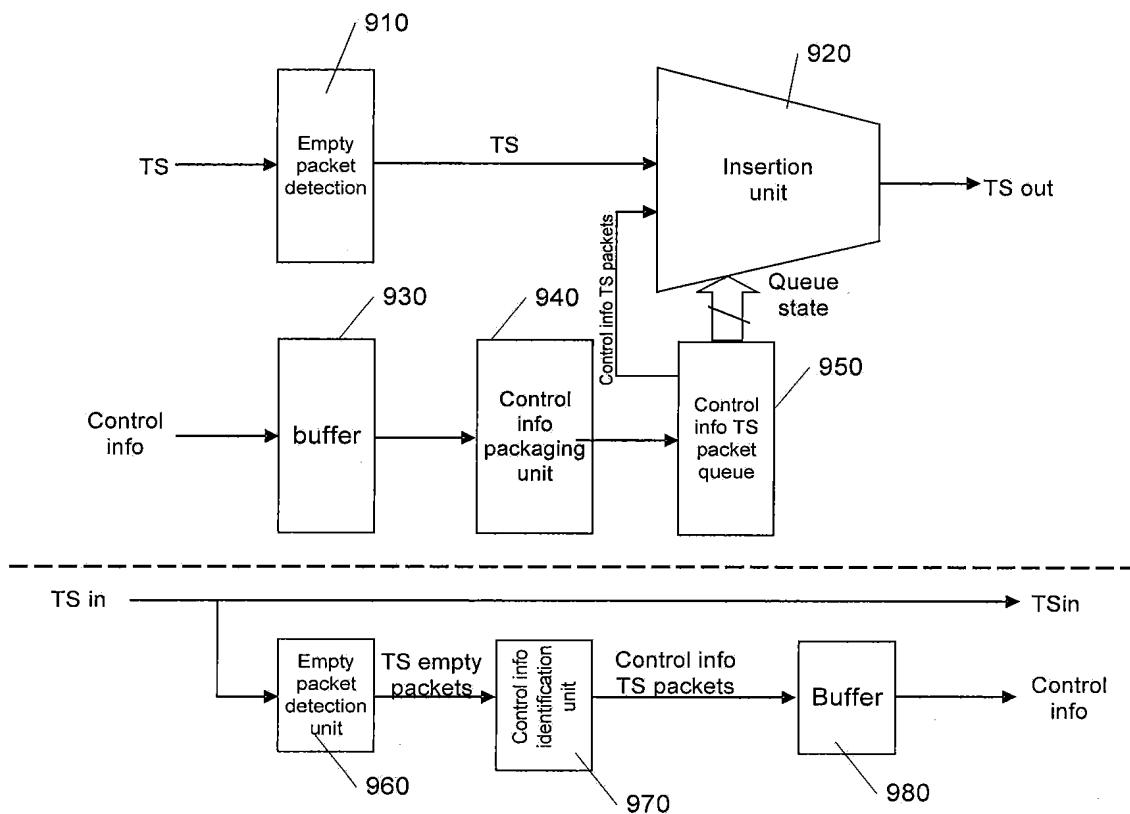


Fig.9

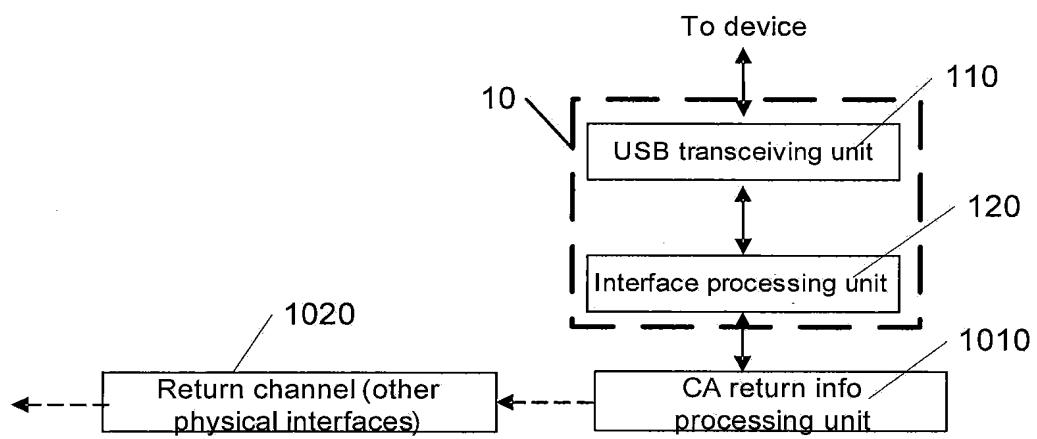


Fig.10

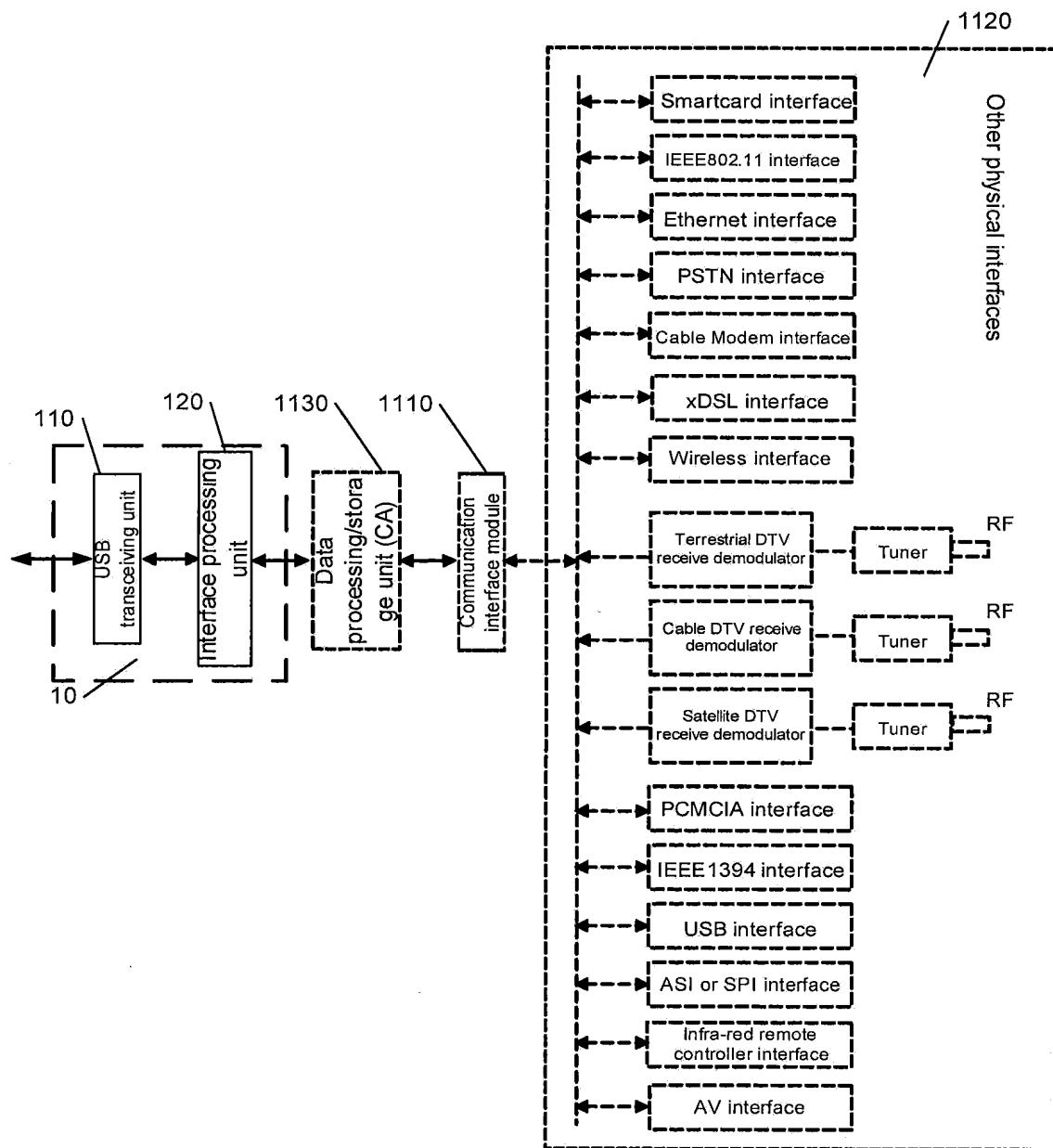


Fig.11

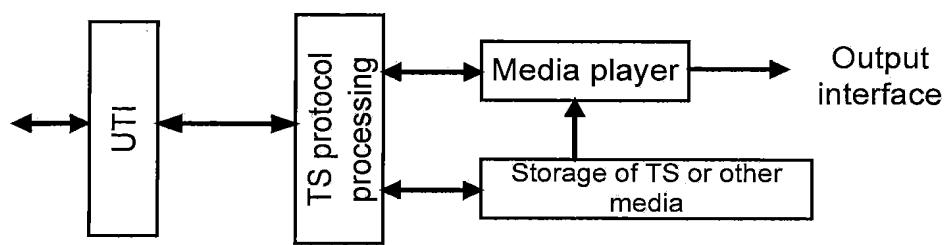


Fig.12

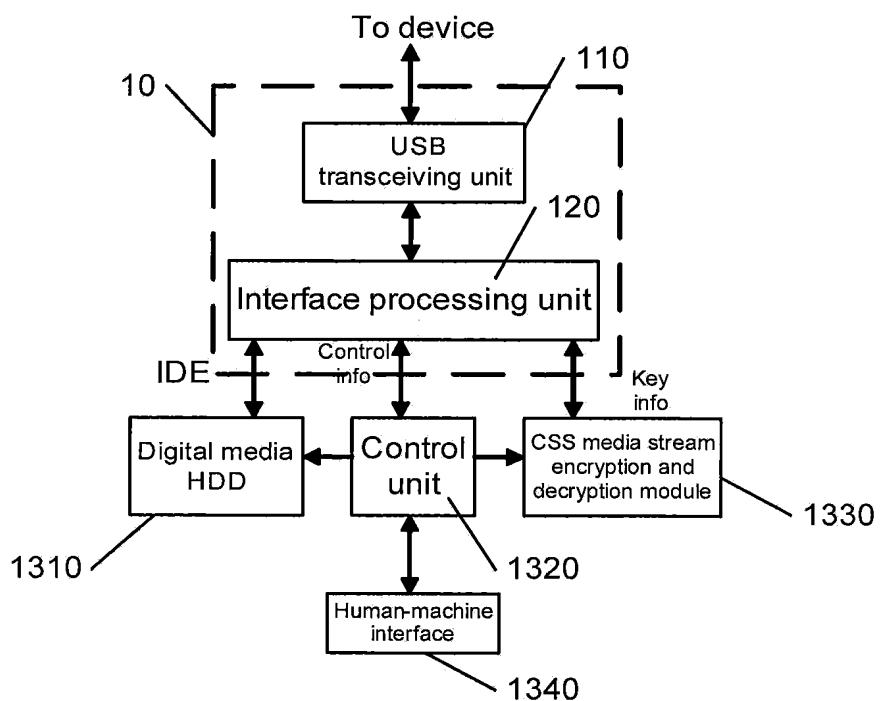


Fig.13

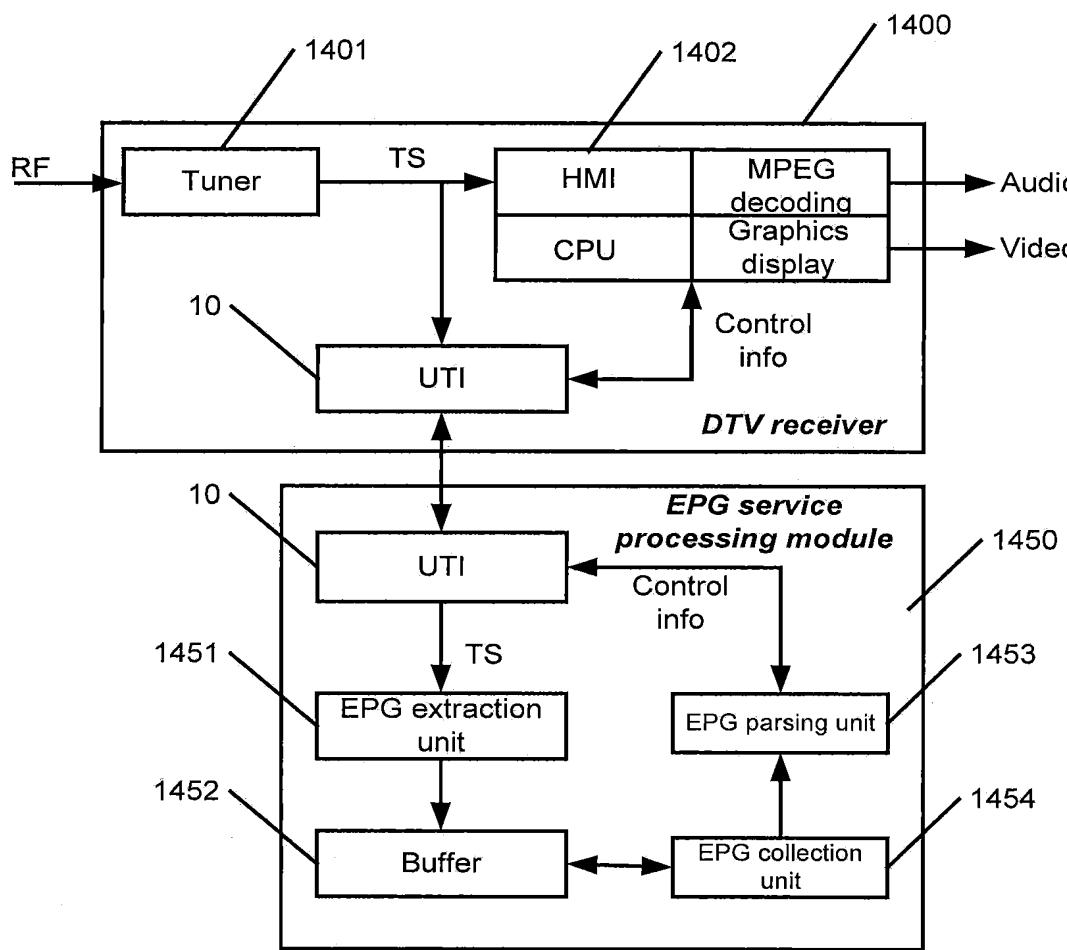


Fig.14

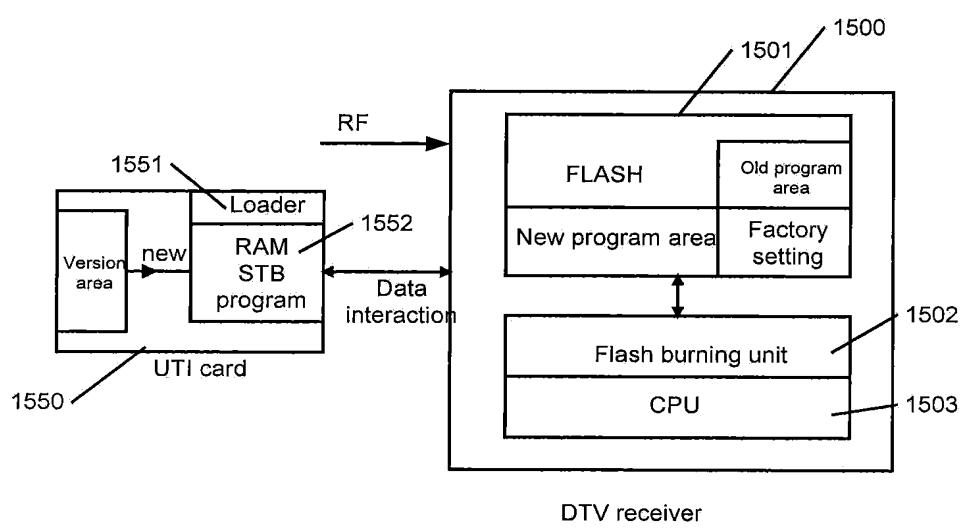


Fig.15

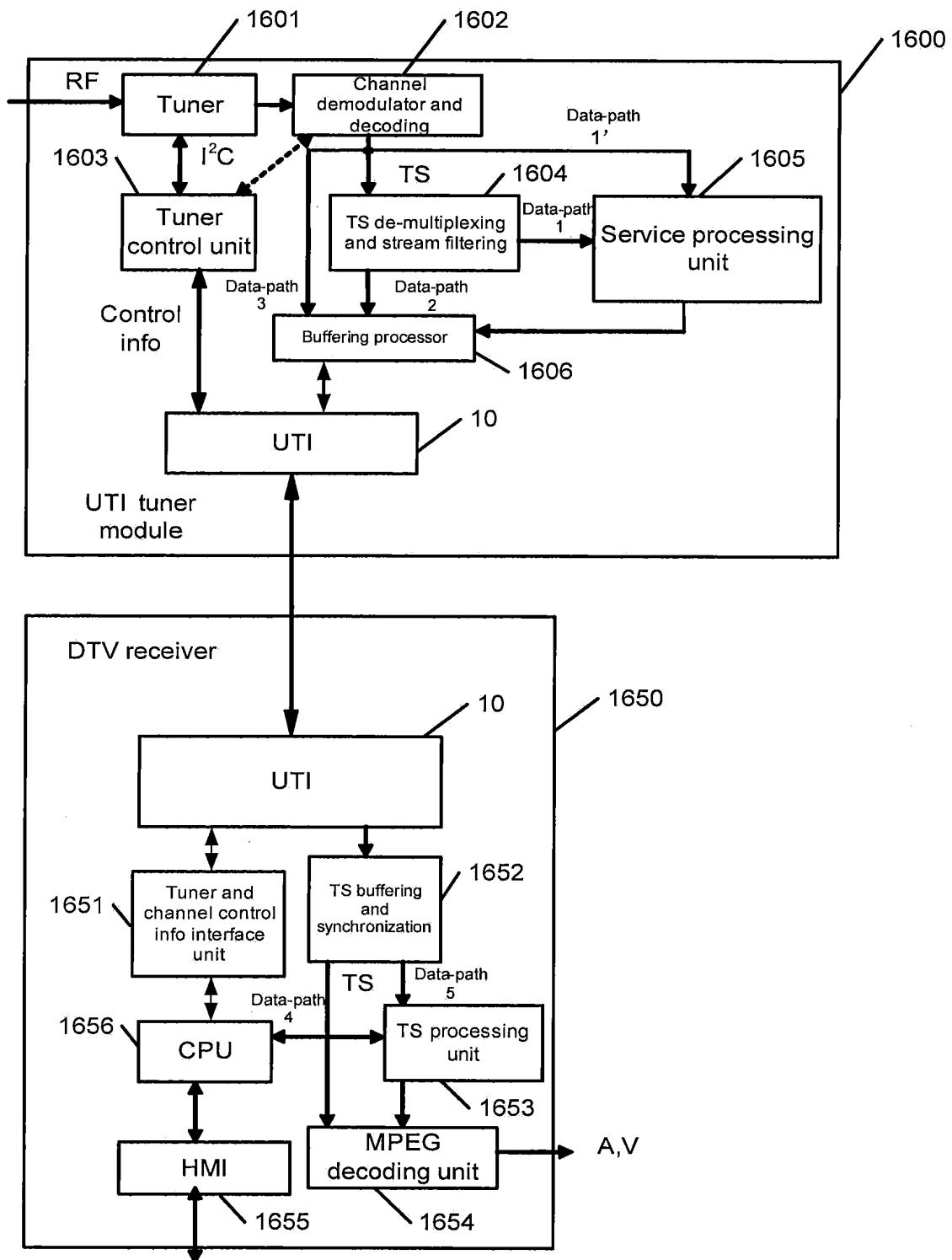


Fig.16

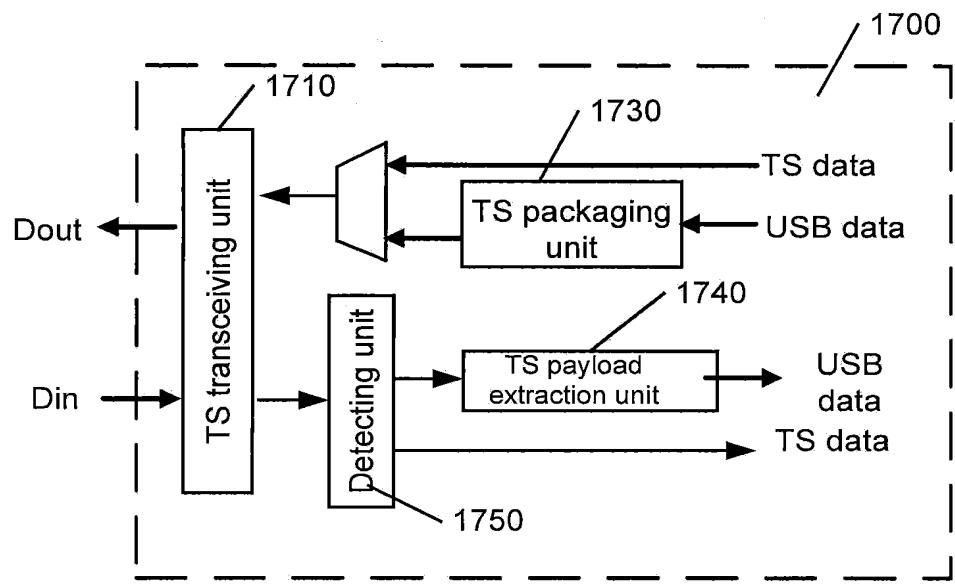


Fig.17

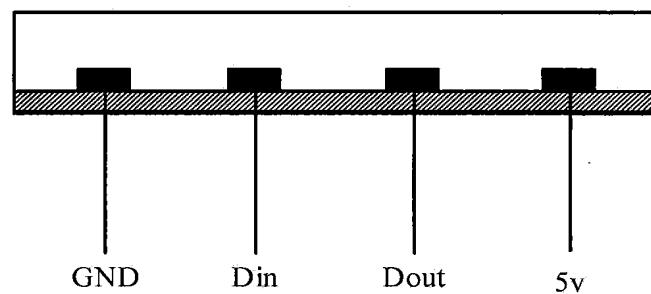


Fig.18

Field	Sync	PID	Payload
Content	0x47	13bits	USB data 184x8bits

Fig.19

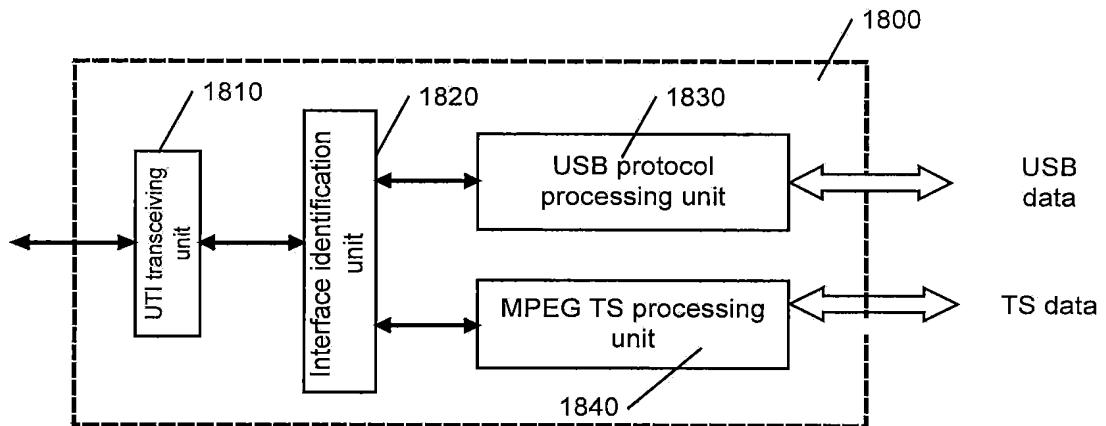


Fig.20

Drawing of the abstract

